Fig. 1(A)

DIAGRAM OF THE PRESENT INVENTION

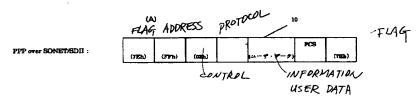


Fig. 1(B)



Fig. 1(C)

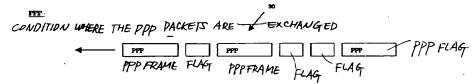


Fig. 1(D)

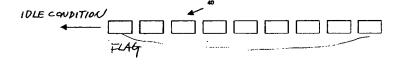


Fig. 2

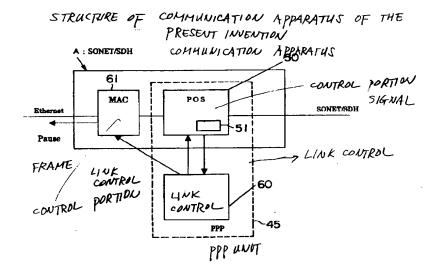
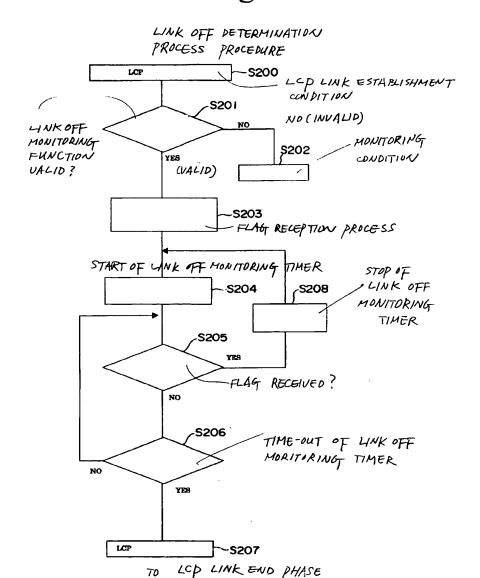


Fig. 3



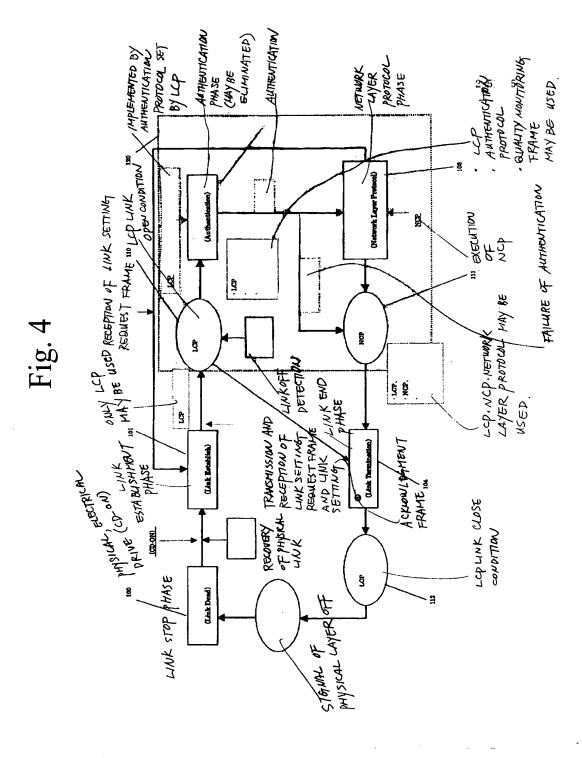


Fig. 5

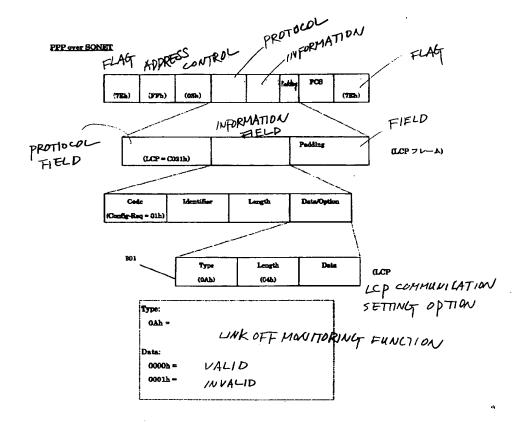


Fig. 6

CASE WHERE THE SETTING FOR VALID LINE OFF MONITORING FUNCTION IS ACCEPTED.

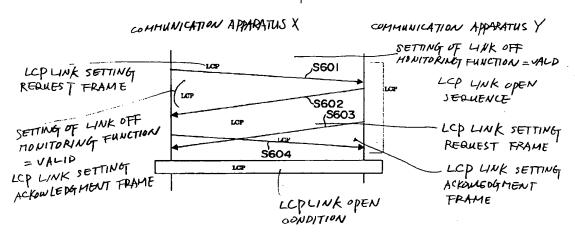


Fig. 7

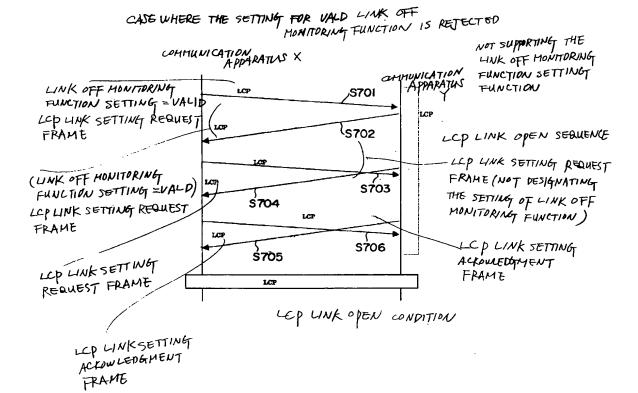
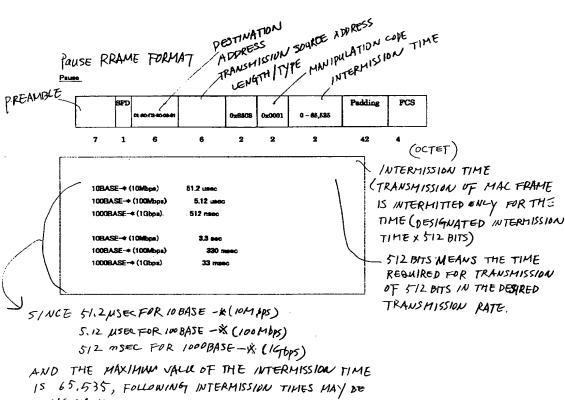


Fig. 8



DESIGNATED:

ON 0133 DEC FOR 10 BASE - & (104bps)

IN 330 111 SEC FOR 100 BASE-X (108 MOPS)

On 33 MSEL FOR 1000 BASE-X. (1965)

HERE, INTERMISSION TIME = 0 IS USED TO INSTRUCT THE RE-START OF THE TRANSMISSION,

Fig. 9

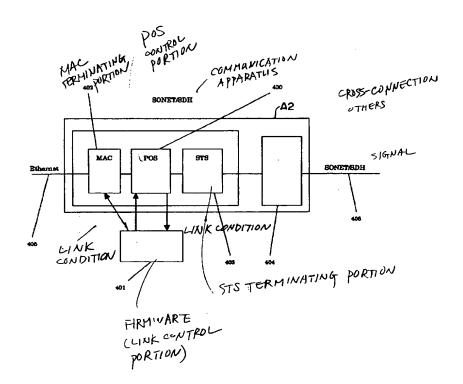


Fig. 10

STRUCTURE OF PRIOR ART

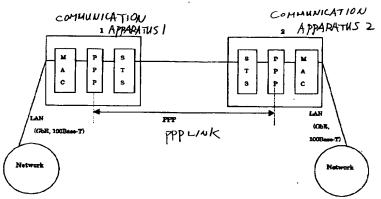


Fig. 11

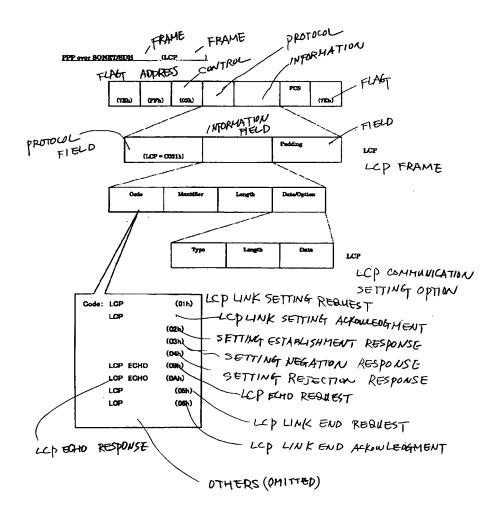


Fig. 12

